

Design and Implementation of Low Pass Filter using Microstrip Line

Prachi Tyagi

*Department of Electronics and Communication Engineering
Krishna Institute of Engineering and Technology, Ghaziabad, India*

Abstract- In this paper we describe the design of microwave low pass filter by using micro strip layout. Most of the communication system has an RF front end that performs signal processing with RF filters. Micro strip filters provides a low cost means for this. Filters play an important role in microwave applications. Microstrip filter is a two port network used to control the frequency response. There is an increasing demand for newer microwave and millimeter-wave systems to meet the emerging telecommunication challenges with respect to size, performance and cost. This paper describes the design of low cost and low insertion loss S-band Low Pass Filter (LPF) by using micro strip layout which works at 2.2 GHz for permittivity 4.4 value with a substrate thickness 0.6 mm for order $n=3$. After the development of microstrip filter we simulate it by using 3D full wave electromagnetic simulator IE3D. Photolitho-graphic process is used for fabrication and after fabrication final testing had done by using the spectrum analyzer.

Keywords: Low pass filter (LPF), IE3D software, Microstrip filter, Vector Network Analyzer.

I. INTRODUCTION

In order to get advantages of larger bandwidth and smaller device size as compare to wireless links microwave communication systems are expanding rapidly towards high frequency such as S-band. Such type of filters is realized with microstrip lines. The goal is to achieve high accuracy in obtaining the desired cut-off frequency and return loss. Microstrip line is a good candidate for the designing of filters due to the advantages it provides like low cost, light weight, compact size, planar structure and easy integration with other components on a single board. Here we use Stepped impedance low pass microstrip filters, because they offer better stop band characteristics and are simpler to design. It use a cascaded structure of alternating high and low impedance transmission lines. It act as semi lumped elements due to much shorter size than the associated guided wavelength. The design and simulation are performed using 3D full wave method of moment based electromagnetic simulator IE3D.

II. FILTER DESIGN METHOD

The design of low pass filters involves two main steps. The first step is to select an appropriate low pass prototype. The choice of the type of response, including the number of reactive elements (order of the filter) and pass band ripple will depend on the required specifications. The element values of the low pass prototype filters, which are usually normalized to make a cutoff frequency $\Omega_c=1.0$ GHz and a source impedance $g_0=1$, are then transformed to the L-C elements for the desired cutoff frequency and the desired source impedance, which is normally 50 ohms for micro strip filters. After obtaining a suitable lumped element filter, the second step is to find an appropriate micro strip realization that approximates the lumped element filter. The design procedure for conventional microstrip butterworth low pass filter of order $n = 3$ (3-pole low pass filter) are as follows:-

1) Filter design specifications:

Cut-off frequency, $f_c = 2.2$ GHz
Substrate height, $h = 1.2$ mm
Dielectric constant, $\epsilon_r = 4.4$
Loss tangent, $\delta = 0.02$
Characteristic impedance, $Z_0 = 50 \Omega$

Highest line impedance, $Z_H = Z_{OL} = 110 \, \Omega$

Lowest line impedance, $Z_L = Z_{OC} = 110 \, \Omega$

Normalized frequency, $\Omega_C = 1$

- 2) For *low pass filter the element* values for $n = 3$,
 $g_1 = 1$, $g_2 = 2$, $g_3 = 1$
- 3) Using *element transformation* we have,
 $L_1 = L_3 = 3.617 \, \text{nH}$, $C_2 = 2.894 \, \text{pF}$
- 4) The *width of capacitor and inductor* is calculated by using,
 For $W/h < 2$

$$W/h = \frac{8 \exp(A)}{\exp(2A) - 2}$$

where, $A =$

and for $W/h \geq 2$

$$W/h = \frac{2}{\pi} \left\{ (B - 1) - \ln(2B - 1) + \frac{\pi^2}{2B} \right\}$$

where, $B =$

- 5) The *effective dielectric constant and characteristic impedance* is calculated by using formula,
 For $W/h \leq 1$

$$\epsilon_{\text{eff}} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \left\{ \left(1 + 12 \frac{h}{W} \right)^{-0.5} + 0.04 \left(1 - \frac{W}{h} \right)^2 \right\}$$

$$Z_c = \frac{120\pi}{\sqrt{\epsilon_{\text{eff}}}} \ln \left(\frac{8h}{W} + 0.25 \frac{W}{h} \right)$$

where $\eta = 120\pi$ ohms is the wave impedance in free space.

For $W/h \geq 1$

$$Z_c = \frac{120\pi}{\sqrt{\epsilon_{\text{eff}}}} \left\{ \frac{W}{h} \right\}$$

- 6) The *effective wavelength* is also find out using formula,

$$\lambda_{\text{ge}} = \frac{\lambda_0}{\sqrt{\epsilon_{\text{eff}}}}$$

- 7) *Calculated dimensions* of the filter:

Tab 1:- Dimensions of 3-pole microstrip low pass filter.

SECTION	$Z_i = Z_L \text{ or } Z_H (\Omega)$	$W_i (\text{mm})$	$L_i (\text{mm})$
1	120	0.4	4.8
2	20	9.2	9
3	120	0.4	4.8
4	50	1.1	3.1

III. EXPERIMENTAL STIMULATION AND MEASURED RESULT

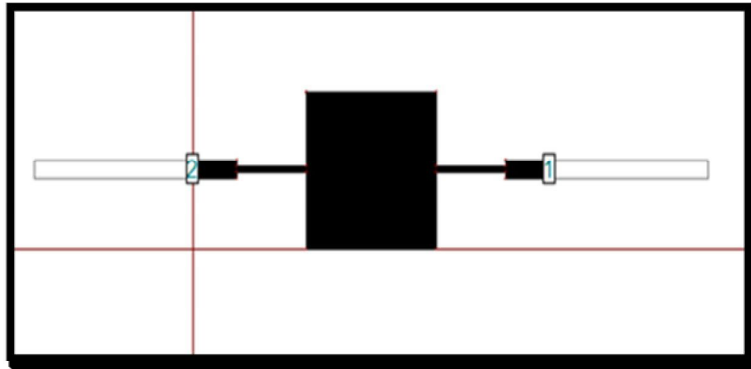


Fig 1:- Layout of 3-pole microstrip low pass filter at cut-off frequency 2.2 GHz.

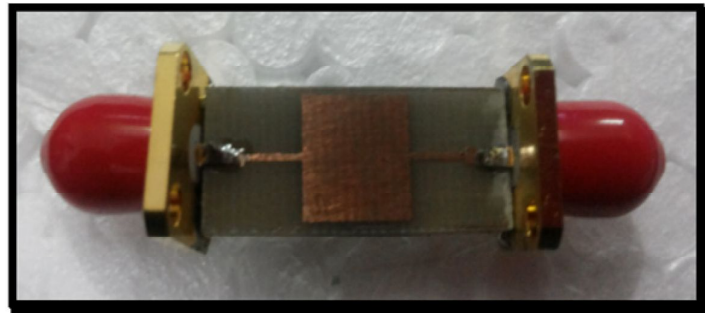


Fig 2:- Fabricated 3-pole microstrip low pass filter at cut-off frequency 2.2 GHz.

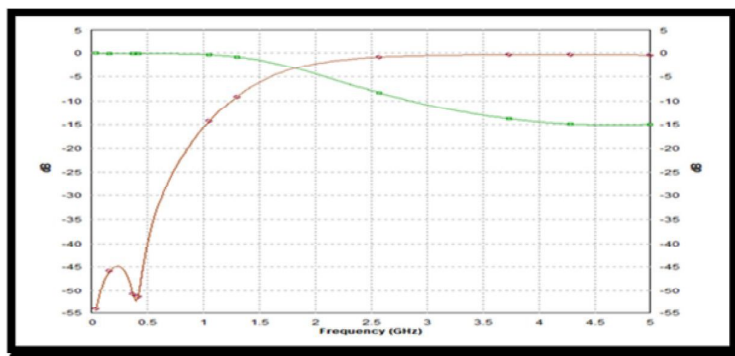


Fig 3:- IE3D simulated result of 3-pole microstrip low pass filter



Fig 4:- Network analyzer tested result of 3-pole microstrip low pass filter.

IV. APPLICATION

Microstrip line is an electrical transmission line which is very useful in conveying microwave frequency signal. Microstrip lines are useful in the analysis of superconducting microwave structures. Recently they are being used increasingly in VLSI circuit design and also in computer package design. It is of compact size therefore very suitable for realization and integration in wireless system.

V. CONCLUSION

A low pass filter using stepped impedance method of microstrip line has been designed and simulated using IE3D software. Then fabricated and being tested using network analyzer. The value of cut-off frequency achieved after testing is lower than the design parameter. This in-accuracy may be because of imperfect fabrication or connection of SMA connector.

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