A New Strategy for Voltage Control in Induction Generators by POD Based Unified Power Flow Controller

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Abstract- Induction generators are generally used for power generation in remote areas or in a region where the grid cannot provide supply. One of the major problems encountered by induction generators is fault. So we need a proper compensation technique to overcome this barrier. A solution to this problem is to introduce Unified Power Flow Controller (UPFC) on its load side. A new technique for the control of gate pulse to VSI of UPFC with the help of Power Oscillation Damping (POD) controller is suggested in this paper. The whole system is modeled in MATLAB/SIMULINK Platform. This paper deals with the power quality improvement and reduction in harmonics by FACTS devices, mainly Unified Power Flow Controllers. The suggested technique presents good quality results in the system.

Keywords – UPFC, POD, VSI, FACTS.

I. INTRODUCTION

Induction Generators are commonly used for the generation of power using wind and micro hydel in remote areas. Fault is one of the main problems that are to be considered for power generation in remote areas. The key factor that is to be considered while connecting a wind turbine system to grid is the Voltage Control [1]. Two main requirements need to be satisfied when an induction generator is connected to grid they are a) reactive power control during normal operating condition, and b) Fault ride through (FRT) capability during fault condition. FRT capability ensures the connection of wind turbine generators to the grid which is ensured by (FRT). Optimum conversion efficiency can be achieved from wind's kinetic energy to the electrical energy, modern variable speed wind turbines [2] (VSWT) with the help of power electronic converters they are capable of varying their speed. When a fault occurs on the load side of induction generators there will be a sudden drop in voltage which is an undesirable condition while considering the fault ride through capability. It should be connected to grid during fault so we are using compensation by Flexible AC Transmission devices (FACTS) on its load side. When a fault occurs the facts devices will inject a voltage or current for necessary compensation.

Based on their connection FACTS Controllers can be classified in to two; shunt connected compensating devices and series connected compensating devices. UPFC [3] is a combination of shunt connected and series connected device with a common dc link capacitor between the shunt and series devices. It consists of a voltage source converter which constitutes IGBTs. Different control methods are used to control the gate pulse of IGBTs. The commonly used controllers for the control of gate pulses are proportional controller, proportional plus integral plus derivative controller. In this paper, a new control strategy for control of gate pulse for IGBT is introduced with POD controller.

Design of controllers has a significant role in the modeling of FACTS controllers. With the help of designed controllers and DC link voltage and depending on voltage variations during fault a suitable DC link voltage is applied. During the time of fault there will be sudden drop in voltage known as voltage sag occurs at that time harmonics will dominate [4]. Controllers are designed based on the values of DC link voltage. This paper utilizes a model of UPFC to study about stability phenomenon and the performance of Induction generator. The UPFC is equipped with a Power Oscillation Damping (POD) control function.

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II. PROPOSED SYSTEM
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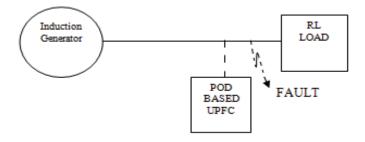


Fig.1 Proposed System with UPFC

Fig1 shows the proposed system with UPFC with POD controller. Induction generator is connected to RL load under normal operating conditions. When a 3 phase fault is applied at t=0.1 sec and 0.2 sec duration of fault is 0.1 occurs there will be sudden voltage sag occurs at load. Thus UPFC will provide necessary compensation with the help of POD controller.

III. UNIFIED POWER FLOW CONTROLLER

UPFC are being used extensively in power systems among Flexible ac transmission systems (FACTS) based power electronic converters because of their ability to provide flexible power flow control. A schematic of the UPFC [2] is shown in Fig. 2. It consists of two voltage source inverter (VSI). One is a shunt VSI and the other is series VSI. The shunt and series VSI are connected via a DC link, which includes a DC capacitor (C). The shunt converter of UPFC controls the connected UPFC bus and DC link capacitor voltage. The series converter of UPFC controls the line active and reactive power flow by injecting a series voltage of adjustable magnitude and phase angle. Under steady state condition, series converter provides the main function of UPFC [5] by injecting a voltage the UPFC is the only device which can fulfill all these functions and thereby meet multiple control objectives by adding the injected voltage V_{pq} , with appropriate amplitude and phase angle, to the terminal voltage V_0 (Using phasor representation, the basic UPFC power flow control functions.

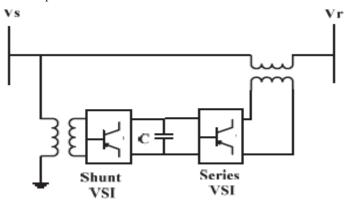


Fig. 2 UPFC electric circuit under normal condition

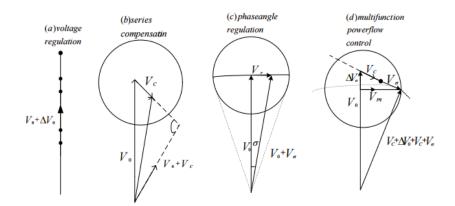


Fig.3. A phasor diagram illustrating UPFC operation

 V_S represents sending end voltage, V_r receiving end voltage, V_{pq} is UPFC voltage, which is inserted in the power system through the series transformer, X is transmission line reactance and I is the line current. The UPFC Series side and shunt active and reactive power flow are given by

$$Psi = r_{bs}V_iV_jsin(\theta_i - \theta_j + \gamma)$$
⁽¹⁾

$$Q_{si} = rb_s V i^2 cos \gamma$$
 (2)

$$P_{sj=-P_{si}} \tag{3}$$

$$Qsj = -r_{bs}V_iV_jcos(\theta_i - \theta_j + \gamma)$$
⁽⁴⁾

Where γ and r are control variables

At UPFC shunt side the active and reactive power flows are given as

$$P_{i1} = -r_{bs}V_iV_j sin(\theta i j + \gamma) - b_s V_iV_j sin\theta_{ij}$$

$$Q_{i1} = -r_{bs}V_i^2 cos(\gamma) + Q_{conv1} - b_s V_{i2} + b_s V_i V_j cos\theta_i$$
(5)
(6)

Whereas series side equations are

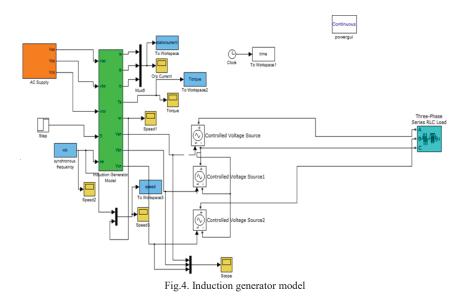
$$P_{j2} = r_{bs} ViV_{jsin}(\theta_{ij} + \gamma) + b_s V_i V_j sin\theta_{ij}$$
⁽⁷⁾

$$Q_{i1} = r_{bs}V_i V_j \cos(\theta_{ij} + \gamma) - b_s V_j^2 + b_s V_i V_j \cos\theta_{ij}$$
⁽⁸⁾

The UPFC injection model is thereby defined by constant series branch susceptance (b_{cs}) which is included in the system bus admittance matrix and bus power injections P_{si} , Q_{si} , P_{sj} , Q_{sj} . If there is a control objective to be achieved, the bus power injections are modified through change of UPFC parameters r, γ and Q_{conv1} .

IV. MODELING OF INDUCTION GENERATOR

Induction generator is a device that converts mechanical energy into electrical energy. It consists of a stationary element called stator and a rotating element called rotor. The stator constitutes insulated copper windings within the stator laminations whereas rotor consists of an aluminum or copper 'squirrel cage' within the rotor laminations. Here there is no need for an exciter or voltage regulator.



Induction generator is modelled using MATLAB/SIMULINK with the help of flux equations given below: The equations for flux linkages [7] are:

$$\frac{dF_{qs}}{dt} = \omega_b \left[V_{qs} - \frac{\omega_e}{\omega_b} F_{ds} + \frac{R_s}{X_{ls}} \left(\frac{\dot{X}_{ml}}{X_{lr}} F_{qr} + \left(\frac{\dot{X}_{ml}}{X_{ls}} - 1 \right) F_{qs} \right) \right] \dots (7)$$

$$\frac{dF_{ds}}{dt} = \omega_b \left[V_{ds} + \frac{\omega_e}{\omega_b} F_{qs} + \frac{R_s}{X_{ls}} \left(\frac{\dot{X}_{ml}}{X_{lr}} F_{dr} + \left(\frac{\dot{X}_{ml}}{X_{ls}} - 1 \right) F_{ds} \right) \right] \dots (8)$$

$$\frac{dF_{qr}}{dt} = \omega_b \left[-\frac{(\omega_e - \omega_r)}{\omega_b} F_{dr} + \frac{R_r}{X_{lr}} \left(\frac{\dot{X}_{ml}}{X_{ls}} F_{qs} + \left(\frac{\dot{X}_{ml}}{X_{lr}} - 1 \right) F_{qr} \right) \right] \dots (9)$$

$$\frac{dF_{qr}}{dt} = \omega_b \left[\frac{(\omega_e - \omega_r)}{\omega_b} F_{qr} + \frac{R_r}{X_{lr}} \left(\frac{\dot{X}_{ml}}{X_{ls}} F_{ds} + \left(\frac{\dot{X}_{ml}}{X_{lr}} - 1 \right) F_{dr} \right) \right] \dots (9)$$

Equations for mutual flux linkages are given below

$$F_{mq} = X_{ml} \left[\frac{F_{qs}}{X_{ls}} + \frac{F_{qr}}{X_{lr}} \right]$$

$$F_{md} = X_{ml} \left[\frac{F_{ds}}{X_{ls}} + \frac{F_{dr}}{X_{lr}} \right]$$

(11)

Thus the machine is modeled in SIMULINK using the above equations and on the load side of induction generator a 3 phase voltage is applied.

V. PWM GENERATOR FOR UPFC

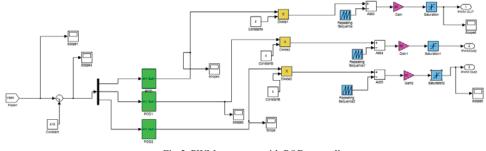
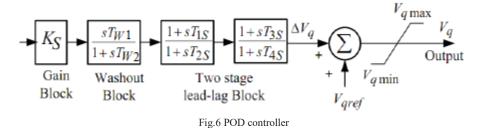


Fig.5. PWM generator with POD controller

UPFC consist of a voltage source converter. For controlling the gate pulse of IGBTs in the VSC PWM generator with POD controller is used the sinusoidal voltage coming out of POD controller is used to compare with triangular wave. The figure shows the modeling of PWM generator for UPFC. Here the voltage is compared with reference signal and given to POD controller and the signal is compared with triangular wave of 20 KHz to produce the gate pulses for the converters. Thus the gate pulse can be controlled.

VI. POD CONTROLLER

The UPFC controller parameters constitute a POD controller[8]. The POD controller consists of a general gain, a low-pass filter, a washout high pass filter, a lead compensator, and an output limiter. But it seems that there is no effect with lag lead filters and on eliminating the system is reduced with a transfer function which is composed of a transducer with a gain which is followed by a washout filter. The main purpose is to remove unnecessary ripples. The signal obtained at the output is limited. The parameters are K, T1, T2, T3 which is used for the control of pulse. The controller is tuned with the values for the voltage source converter. The values are K= 1.1058PU, T₁=0.068Sec, T_{W1} =0.145Sec, T_{W2} = 0.385Sec.



Transfer function constitutes lead-lag filters which can be used for phase compensation of POD controllers. A POD controller is tuned to a value providing necessary damping to stabilize the power system after severe disturbances.

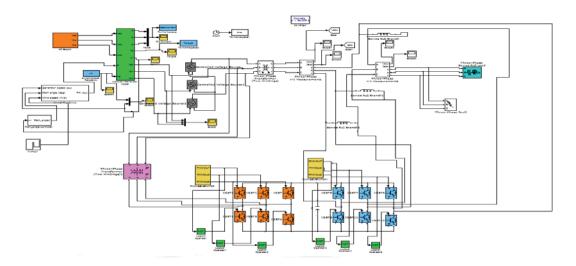
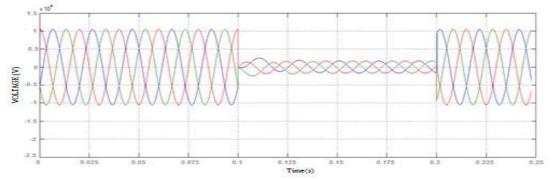


Fig.7. Induction Generator with UPFC

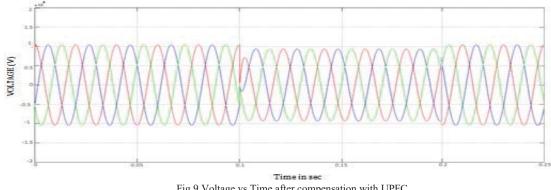
UPFC is connected so that it can provide necessary compensation it is a combination of shunt converter and series converter. In order to have better control over the system shunt converter and series converters are provided with separate gate pulse control.

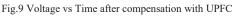
VII. RESULTS AND DISCUSSIONS

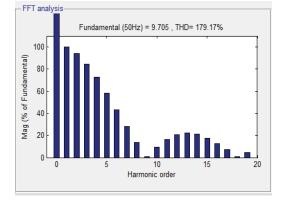
UPFC is a combination of shunt converter and series converter with a common dc link capacitor as a source. During the occurrence of fault power quality problems will dominate and there will be a sudden fall in voltage at the instant between 0.1sec and 0.2sec. Therefore Shunt converter will provide the necessary reactive power need for dc link capacitor whereas series converter will inject a voltage to the system for compensation. At the time of fault voltage has fallen to 1kV but after compensation by UPFC it has raised to 9kV and Total Harmonic Distortion (THD) during fault has improved from 179.17% to 0.76%. By introducing UPFC on the load side of induction generator the stability of induction generator will improve. This assures the FRT capability and improves the power quality.











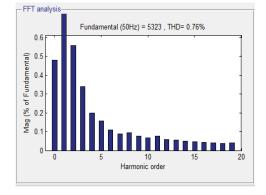


Fig.10. THD before compensation.

Fig.11. THD after compensation with UPFC

VIII. CONCLUSIONS

Fault is one of the severe problems that Induction generators face while generating power in remote areas. There is a sudden increase in reactive power which will cause sudden voltage sag during fault it will cause problems to the load connected to it. So we use UPFC to compensate by injecting a voltage in to the system with the help of shunt converter and series converter and hence necessary compensation can be obtained. The gate pulse of IGBT is controlled by a PWM generator with POD controller. Thus by POD controller serves a major role in improving power quality by eliminating ripples. Hence the compensation is done with UPFC in SIMULINK platform and the problem is completely eliminated which is clear from output waveform and THD values. Hence by reactive power compensation by UPFC the problem is eliminated which is the main aim of the paper.

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